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Mail Stop Patent Application  
P. O. Box 1450  
Alexandria, Virginia 22313-1450

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DATE: January 16, 2004

Sir:

Transmitted herewith for filing is the Patent Application of:

Inventors: Rajat Chaudhry, Brian King Flachs, Daniel Lawrence Stasiak

For: Method and Apparatus for VLSI Clock Gated Power Estimation Using LCB Counts

Assignee Name: International Business Machines Corporation

Assignee Residence: Armonk, New York

Enclosed are:

- X 17 Patent Specification including an Abstract  
X 6 Pages of Claims  
X 5 Sheet(s) of drawings.      Informal X Formal  
X      Declaration(s) and Power of Attorney  
X      Form PTO 1595 and Assignment of the Invention to IBM Corporation  
     A certified copy of a      application.  
     Information Disclosure Statement, PTO 1449 and copies of      references.

The filing fee has been calculated as shown below:

FOR	Number Filed	Number Extra	Rate	Basic Fee \$770.00
Total Claims	24 -20 =	4 X	\$ 18	= \$ 72.00
Independent Claims	<u>3</u> - 3 =	<u>0</u> X	\$ 86	= \$ <u>0</u> .00
Multiple Dependent Claims	00	X	\$290	= \$
Total Filing Fee				= \$ <u>842</u> .00

- X Please charge my Deposit Account No. 09-0447 in the amount of \$ 842. A duplicate copy of this sheet is enclosed.
- X The Commissioner is hereby authorized to charge payment of the following fees associated with this communication or credit any overpayment to Deposit Account 09-0447. A duplicate copy of this sheet is enclosed.
- X Any additional filing fees required under 37 CFR 1.16.
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Respectfully submitted,

By

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